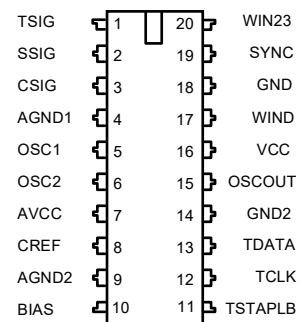

**UNIVERSAL TELETEX SYSTEMS
Teletext DATA SLICER
CF72417**

Draft 1

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- Extracts Teletext , WSS and VPS data
- Forms a custom 2-chip solution when used with a EUROTEXT teletext decoder
- Low power 1 μ CMOS (<100mW)
- Standard 20 pin/300mil package
- Tolerates a range of video distortions
- Operates with 13.875 MHz fundamental mode crystal



CF72417

20-pin/300 mil N Plastic

DESCRIPTION

The CF72417 is a new device comparable with the currently available CF72416 but having the added capability of extracting Wide Screen Signalling (WSS) data. The CF72417 forms the analogue component of the Texas Instruments (TI) teletext system.

The device extracts from the incoming video signal both composite sync and Teletext/VPS/WSS data. From the Teletext/VPS/WSS data it also regenerates the appropriate frequency clock signal. It passes the clock, data and composite sync to the digital decoder chip. The Teletext WSS and VPS use the same data and clock pins, the different signals being identified by the appropriate decoder.

The CF72417 device has the following features and enhancements over the CF72306:-

1. VPS data and clock extraction.
2. WSS data and clock extraction
3. Improved adaptive slicing level for the Sync separator.
4. Minor board changes are required to upgrade to the CF72417 from the CF72306.

The CF72417 device has the following feature and enhancement over the CF72416:-

1. WSS data and clock extraction.
(A minor board change is required to supply the Timing signal for WSS data)

The crystal oscillator is the same as used with the CF72306. The crystal specification is given below:-

13.875MHZ Crystal Specification

- Oscillation Mode.....Fundamental Parallel
- Frequency.....13.8750MHz
- Crystal Frequency Stability.....±150ppm *
- Maximum Crystal ESR (steady-state).....60 Ohms
- Maximum Crystal ESR (start-up).....120 Ohms
- Maximum Shunt Capacitance.....7 pF
- Maximum Motional Capacitance.....30 fF
- Crystal Load Capacitance.....18 pF
- Free Air Operating Temperature Range.....0 to 70 °C
- Drive Level.....1mW max.
- Ageing.....±5ppm max. for first year

* includes temperature stability and manufacturing tolerance.

Internal Block Diagram of CF72417

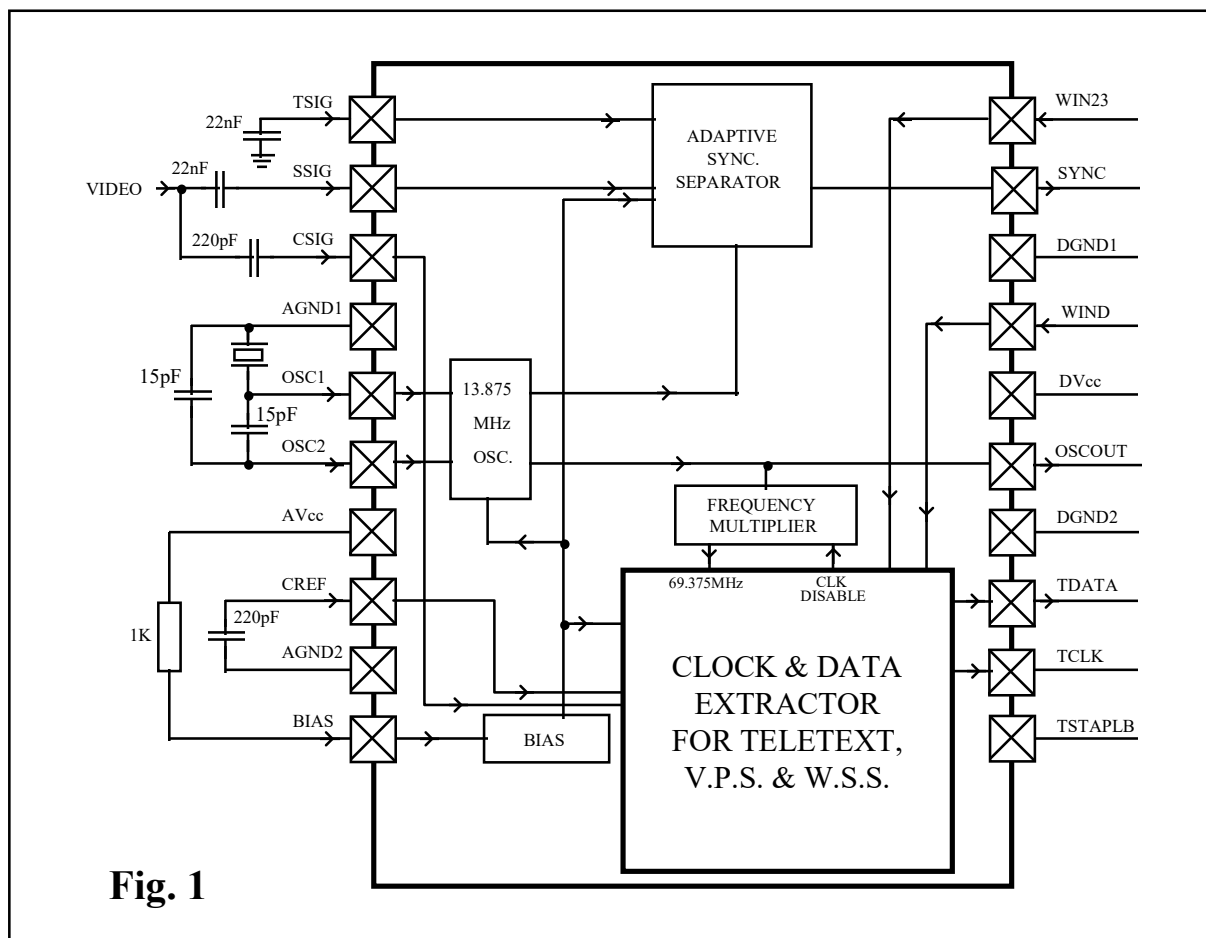


Fig. 1

Pin description

PIN	SIGNAL	DESCRIPTION	I/O	TYPE
1	TSIG	Video Sync Reference	I	Analogue
2	SSIG	Video Sync Input	I	Analogue
3	CSIG	Video Data Input	I	Analogue
4	AGND1	Analogue Ground		Power
5	OSC1	13.875 MHz Oscillator	I	Analogue
6	OSC2	13.875 MHz Oscillator	I	Analogue
7	AVCC	Analogue Vcc		Power
8	CREF	Video Data Reference Input	I	Analogue
9	AGND2	Analogue Ground		Power
10	BIAS	Internal Reference	I	Analogue
11	TSTAPLB	Test/Application	I	Digital
12	TCLK	Teletext Clock	O	Digital
13	TDATA	Teletext Data	O	Digital
14	DGND2	Digital Ground		Power
15	OSCOUT	Oscillator Output	O	Digital
16	DVCC	Digital Vcc		Power
17	WIND	Timing Signal	I	Digital
18	DGND1	Digital Ground		Power
19	SYNC	Separated Sync Output	O	Digital
20	WIN23	WSS Timing Signal	I	Digital

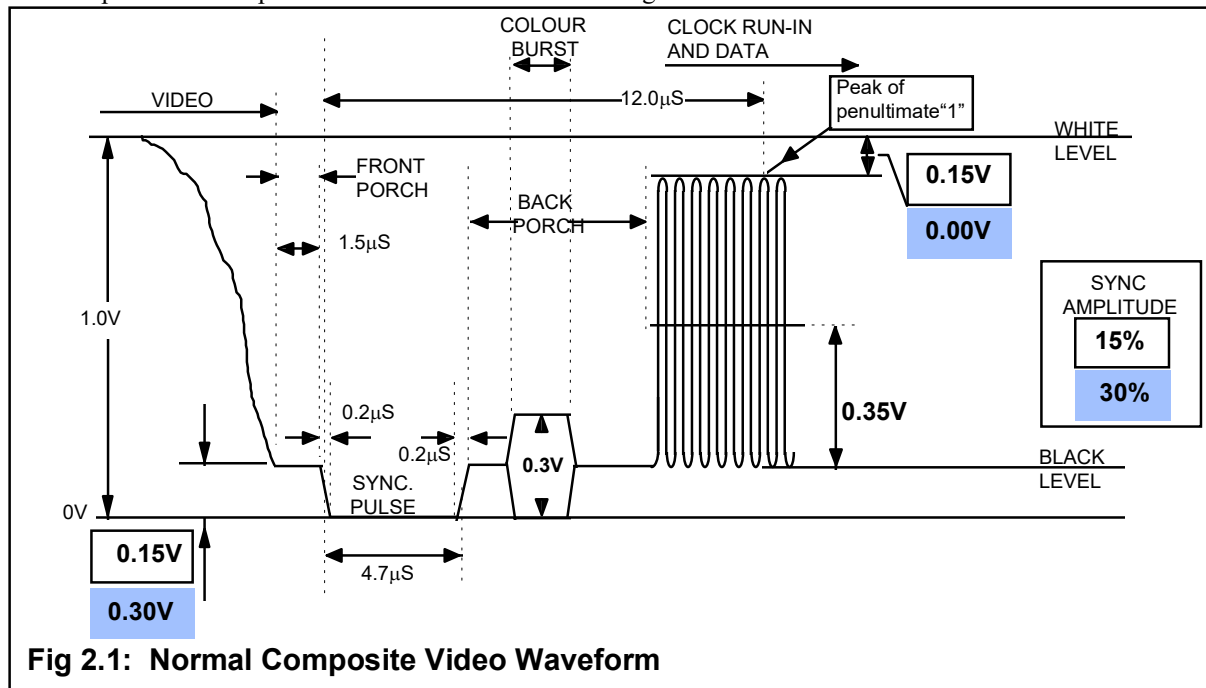
Signal pins = 14

Power pins = 6

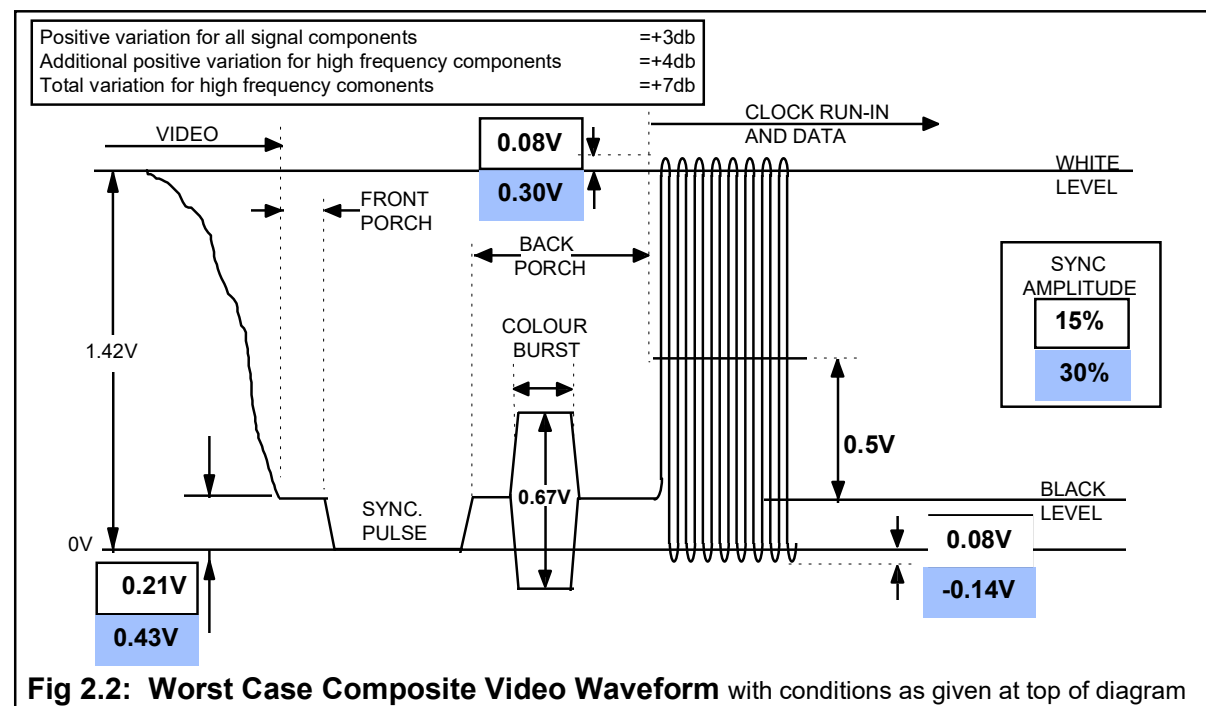
Composite video input description

(Please note that the following diagrams relate to the teletext data line. However, VPS data will exhibit similar degradation.)

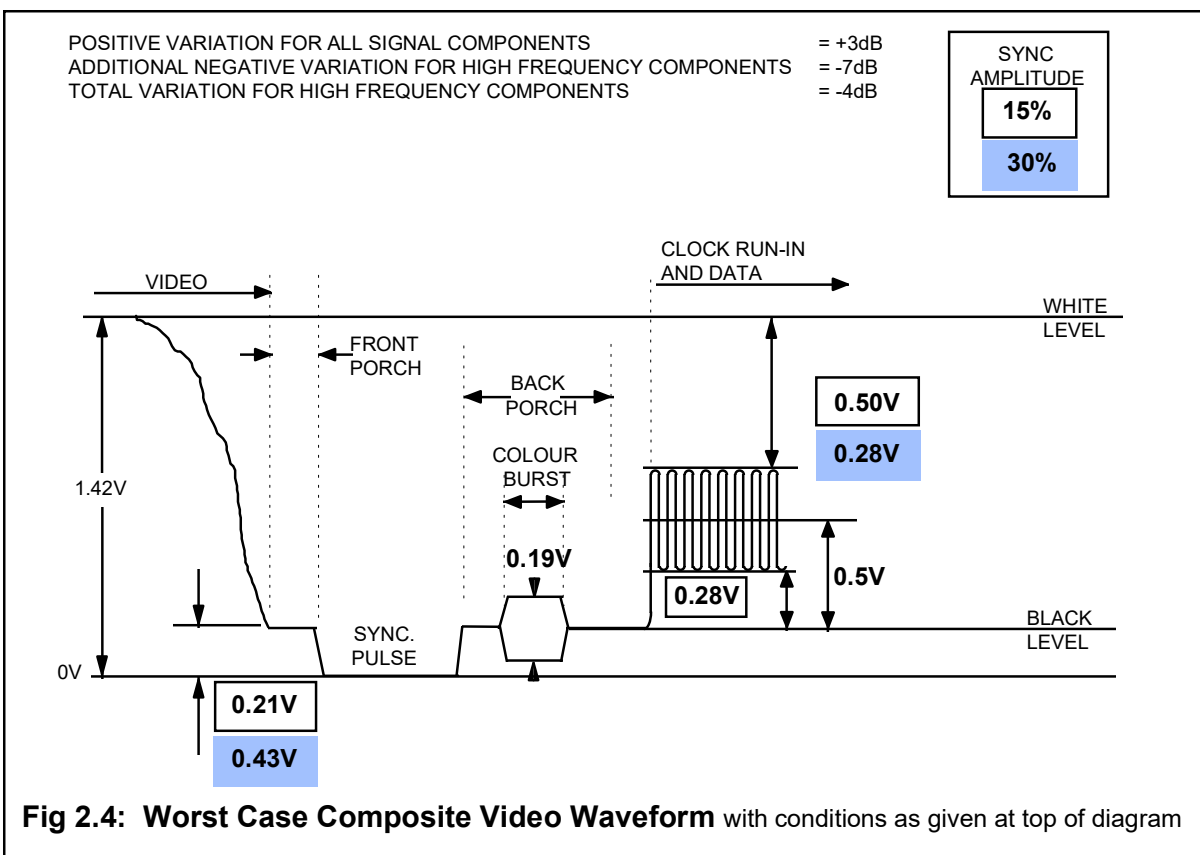
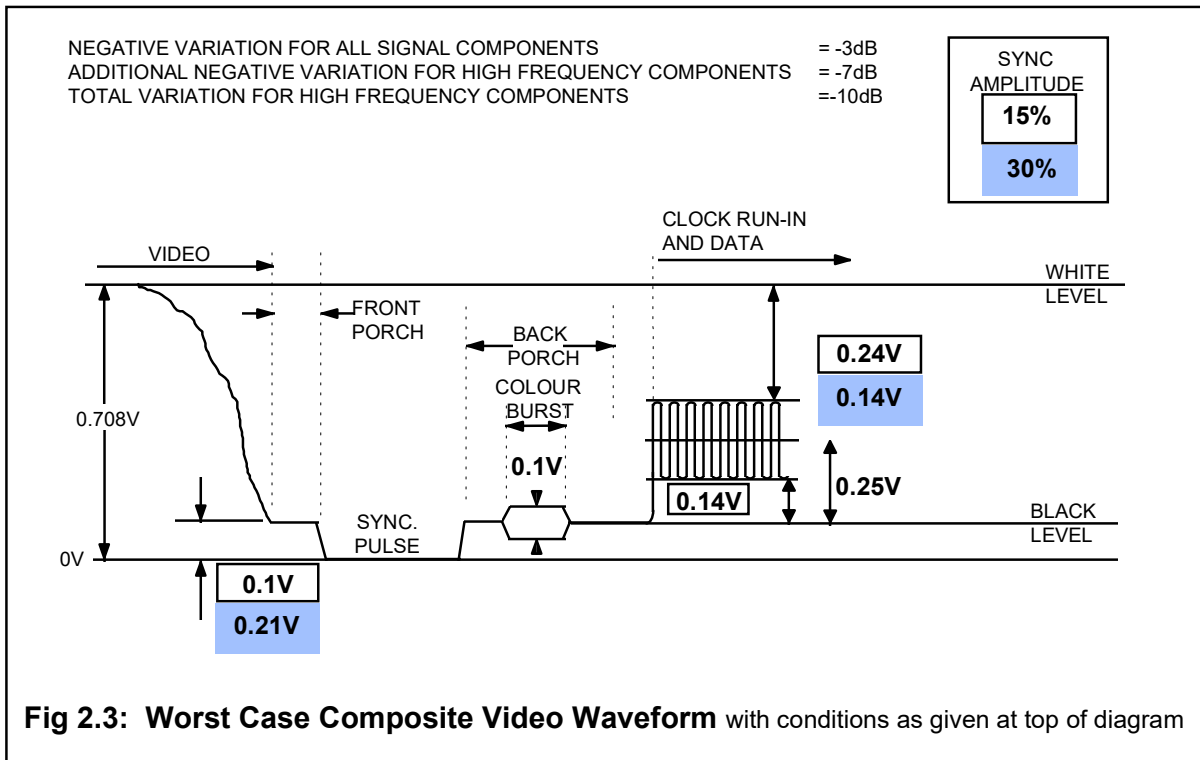
The composite video input to the CF72417 has the following form in the nominal case.



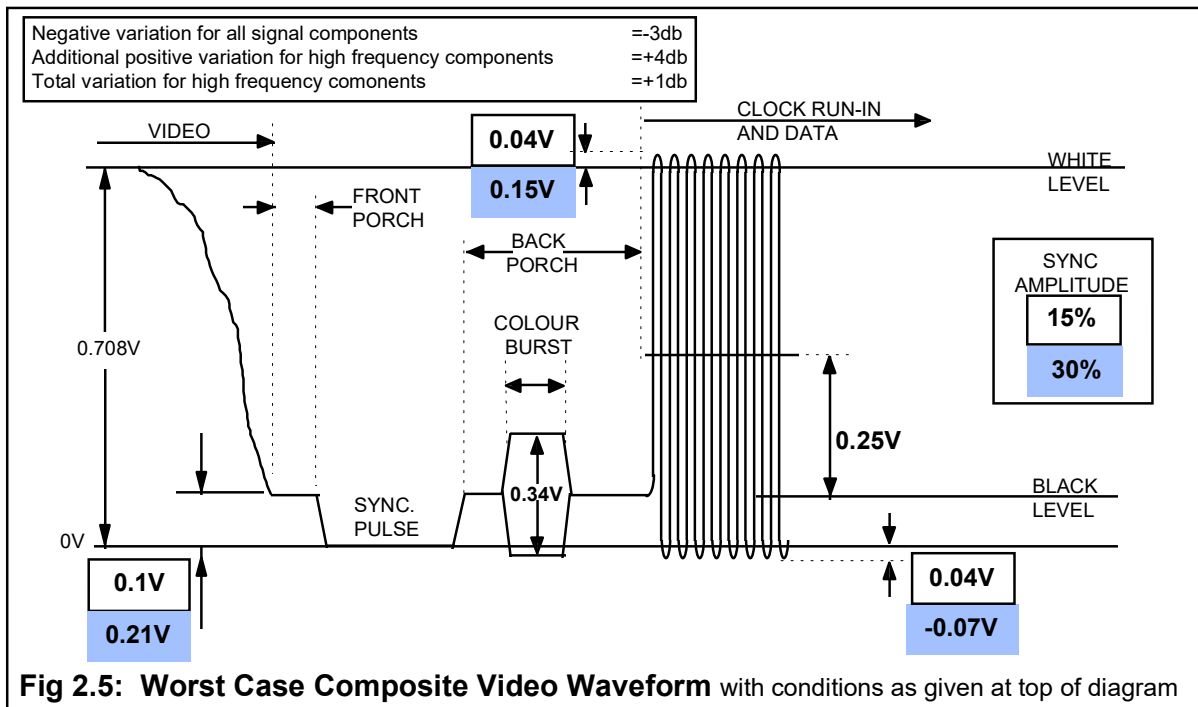
The composite video input to the CF72417, when subject to the worst case amplitude tolerances resulting from both low and high frequency distortions, has the forms shown in the following four diagrams. Parameter values are shown in Table 2.2.



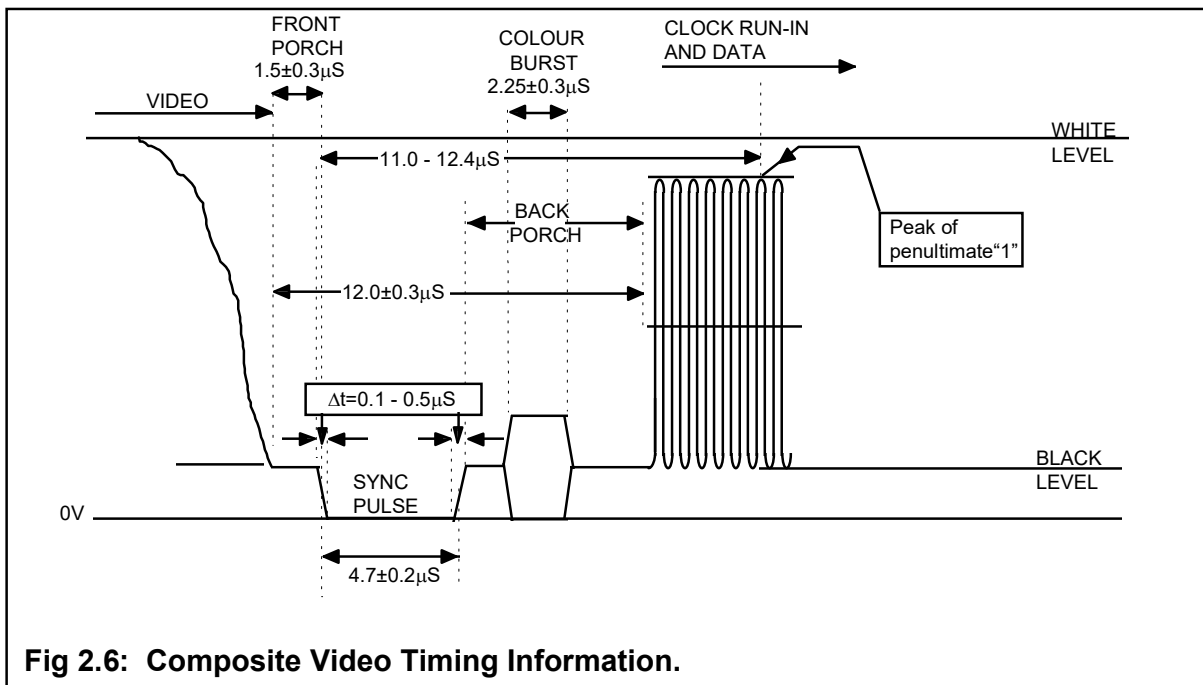
Composite Video input description - continued.



Composite video input description - continued



The composite video input to the CF72417 has the following timing specification. All amplitudes in this figure have nominal reference values as shown in Figure 2.1.



VIDEO INPUT SPECIFICATION

PARAMETER	MINIMUM	NOMINAL	MAXIMUM	UNIT	Notes
Composite Video Amplitude(peak-to-peak)	0.708	1.0	1.42	V	
Source Impedance	-	-	350	Ω	
Sync Amplitude	10	-	43	% Nominal Video	1
Sync Duration	4.5	4.7	4.9	μ s	2
Sync Transition Times	0.1	0.2	0.5	μ s	3
Front Porch	1.2	1.5	1.8	μ s	
Under/Overshoots	-	-	20	% Nominal Video	4
Time between -ve sync & penultimate '1' of Clock Run-in	11	12	12.4	μ s	5
Line Blanking	11.7	12.0	12.3	μ s	6
Colour Burst Amplitude(peak-peak)	9	30	67	% Nominal Video	
Colour Burst Frequency	-	4.43	-	MHz	
Colour Burst Duration	1.95	2.25	2.55	μ s	
Clock Run-in Amplitude	22	70	157	% Nominal Video	7
Number of Complete & Consecutive Clock Run-in cycles	4	-	8		8
Bit rate - VPS	2.4997	2.5000	2.5003	Mbps	
Element rate - WSS	2.4997	2.5000	2.5003	Mbps	
Bit rate - Teletext	6.9373	6.9375	6.9377	Mbps	
Data "one" level	19	66	161	% Nominal Video	
Data "zero" level	-6	0	6	% Nominal Video	9

Note 1: Minimum value is: low frequency -3db and 15% sync.

Maximum value is: low frequency +3db and 30% sync

Note 2: As measured at 50% of the total sync. amplitude.

Note 3: As measured at 10% and 90% of the total sync. amplitude.

Note 4: Under/Overshoots are of order of 100nS duration.

Note 5: As measured at 50% of the negative sync transition and the peak of the penultimate "1"

Note 6: As measured at 50% of the negative and positive edges of adjacent peak white components which are respectively before and after a sync pulse.

Note 7: Clock Run-in envelope - Raised cosine filter with centre frequency at 3.46875 for teletext or 2.5000MHz for VPS.

Skew symmetrical about 0.5 x bit rate and substantially reduced at 5MHz.

Note 8: A clock run-in cycle is complete if its amplitude equals or exceeds 22% of composite video.

Note 9: With the black level as a zero reference.

Absolute Maximum Ratings

Supply Voltage, V _{cc}	6.5V
Input Voltage.....	V _{cc} + 0.5V
Current Any signal pin	±20mA
Current V _{cc} or GND	±50mA
Operating free air temperature range	0°C to 70°C
Storage Temperature range.....	-65°C to 70°C
Thermal Resistance θ _{ja}	150°C/W

Recommended Operating Conditions

Symbol	Parameter	Test Conditions	min.	nom	max.	unit
F _o	Operating Freq.			13.875(*)		MHz
Supplies						
V _{cc}	Supply Voltage	V _{ih} = min V _{il} = min	4.5	5.0	5.5	V
Inputs - Digital:- WIND, WIN23, TSTAPLB (CMOS inputs)						
V _{ih}	High Level input Voltage		0.7V _{cc}			V
V _{il}	Low Level input Voltage				0.2V _{cc}	V
I _{ih}	Input Current High	V _{cc} = max.	-1		+1	μA
I _{il}	Input Current Low	V _{cc} = max.	-1		+1	μA
Outputs - Digital:- TCLK, TDATA, SYNC, OSCOUT						
V _{oh}	Output Voltage High	V _{cc} =min I _{oh} =30μA =300μA =3mA	V _{cc} -100mV V _{cc} -200mV 3.76			V
V _{ol}	Output Voltage Low	V _{cc} =min I _{ol} =30μA =3mA			100 500	mV mV

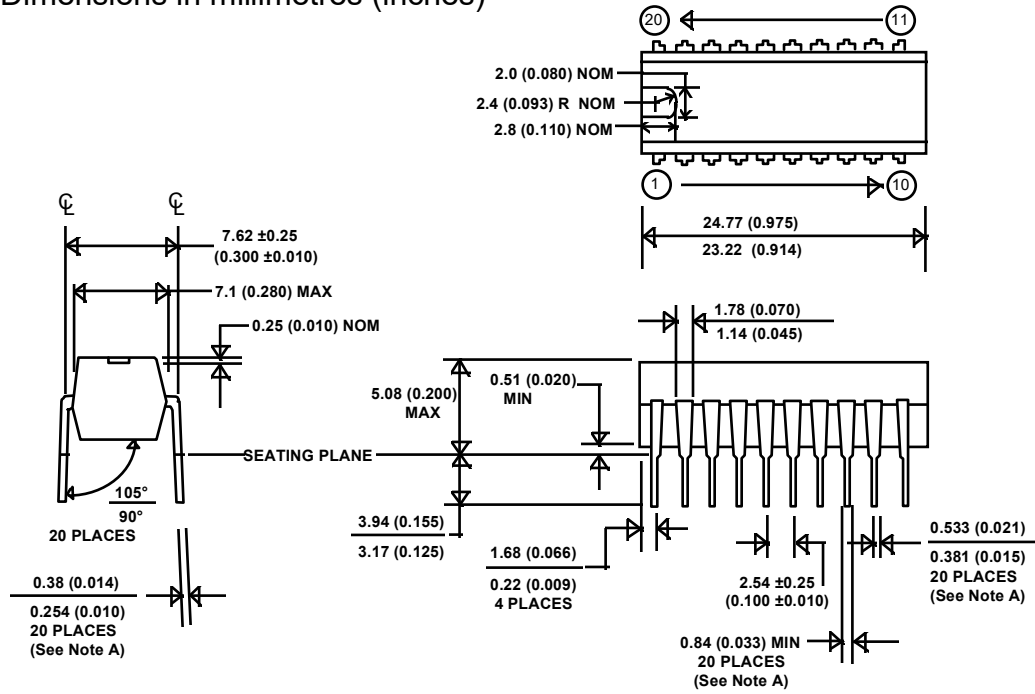
(*) 2 X (Teletext data rate - 6.9375mhz)

Note 1: It is recommended that PCB track capacitance on OSCOUT should not exceed 22pF.

Note 2: It is recommended that PCB track capacitance on TCLK and TDATA should not exceed 33pF

Package Dimensions

20-Pin Plastic Dual-in-line Package
(20-Pin N Plastic)
Dimensions in millimetres (inches)



Note : A. This dimension does not apply for solder dipped leads.

Fig 3.

Applications Circuit with CF70206

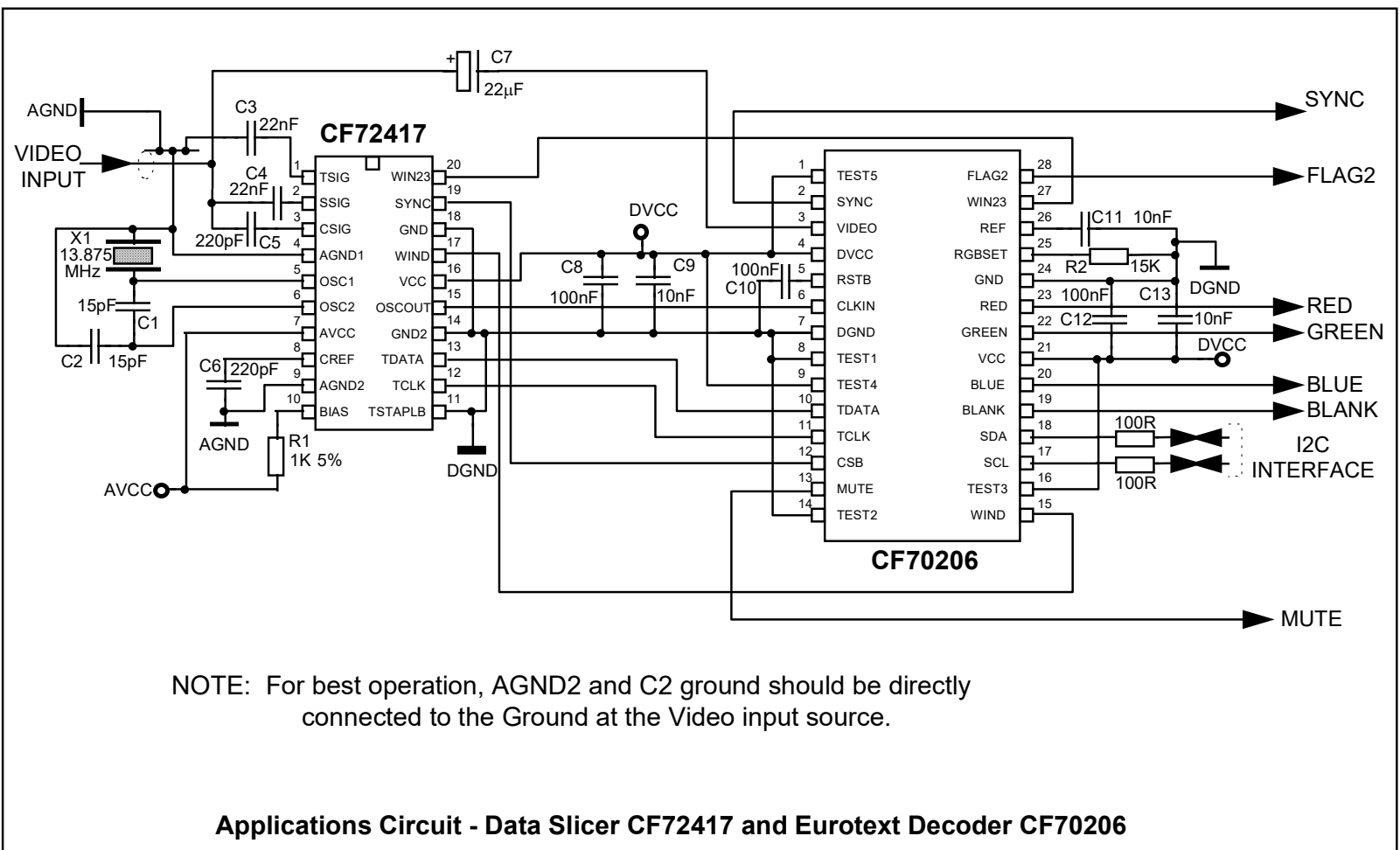


Fig 4.

Supplementary Applications Information

(required for stand-alone applications)

The following information will be required when the Data Slicer is not used in conjunction with a Texas Instruments single or multi-page teletext decoder:-

1. TDATA and TCLK outputs are static low when WIND input is static low or when the VIDEO input contains invalid teletext or VPS clock run-in data.
2. Phase Relationship between TDATA and TCLK signals:

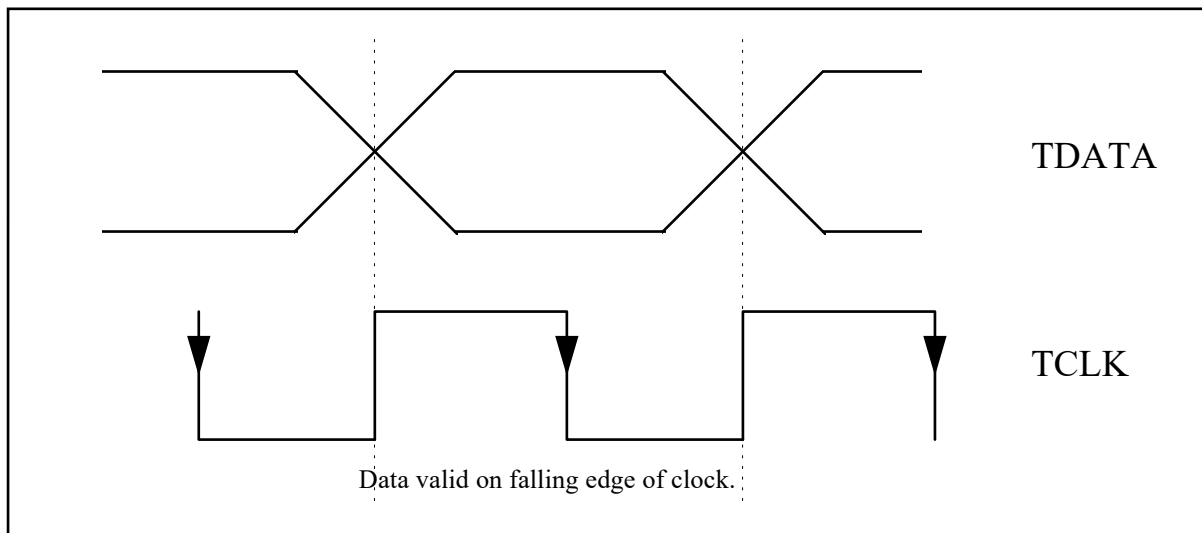


Fig. 5: TDATA and TCLOCK Timing

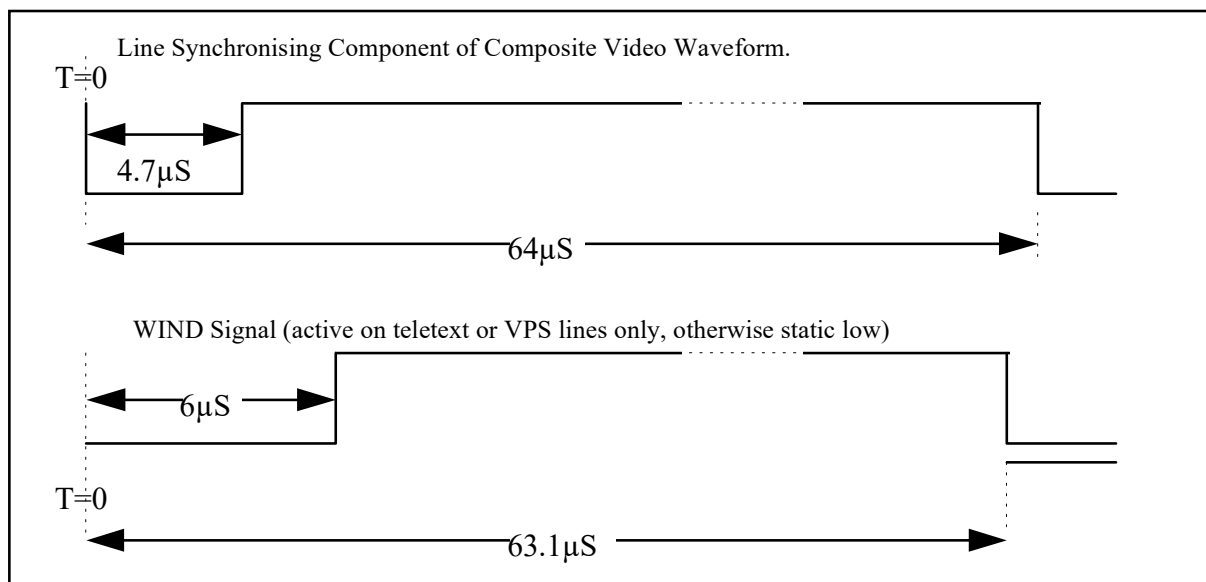


Fig. 6: Window Signal Timing

Notes:

1. T=0, above, can be coincident with, or up to 1.5µS later than, negative video line SYNC edge
2. All timings may sustain a tolerance of ±5%.

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